

APPROVAL SHEET

WF₁₀A

±1%, ±5%

Ultra High Power Chip Resistors

Size 1210, 3/4W



FEATURE

- 1. High reliability and stability
- 2. Reduced size of final equipment
- 3. Ultra high power
- 4. Higher component and equipment reliability
- 5. RoHS compliant and Lead free products

APPLICATION

- Consumer electrical equipment
- Automotive application
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

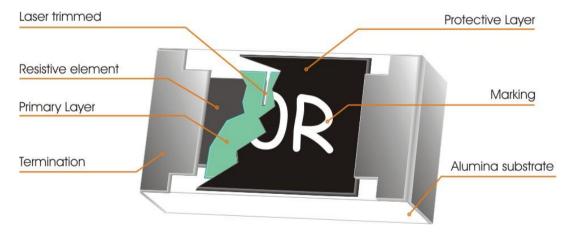


Fig 1. Construction of Chip-R



QUICK REFERENCE DATA

Item	General Specification				
Series No.	WF10A				
Size code	1210 (3	225)			
Resistance Tolerance	±5%, ±1%, ±0.5%				
Resistance Range	1Ω ~ 1	MΩ			
TCR (ppm/°C)	±1%, ±0.5% (E24+E96):	±5% (E24):			
-55°C ~ +155°C	1 ~ 49.9Ω: -200 ~ +500ppm/°C	1 ~ 47Ω: -200 ~ +500ppm/°C			
	51 ~ 1MΩ: ≤ ± 100 ppm/°C	51 ~ 1MΩ: ≤ ± 200 ppm/°C			
Max. dissipation at T _{amb} =70°C	3/4 W				
Max. Rated voltage	200V				
Max. Overload voltage	400V				
Climatic category (IEC 60068)	-55 ~ +155°C				
Basic specification	JIS C 5201-1 : 1998				

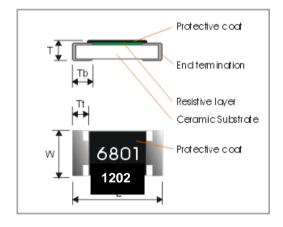
Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage: So called RCWV (Rated Continuous Working Voltage) is determined by

RCWV = $\sqrt{\text{RatedPower} \times \text{Resistance Value}}$ or Max. RCWV listed above, whichever is lower.

DIMENSIONS(unit:mm)

	•
Part No	WF10A
L	3.10 ± 0.15
W	2.50 ± 0.15
t	0.55 ± 0.15
Tt	0.45 ± 0.25
Tb	0.50 ± 0.25





MARKING

1202 = 12Kohm

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of $\pm 1\%$, $\pm 5\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

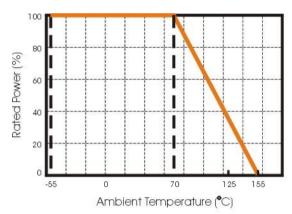


Fig 2. Maximum dissipation in percentage of rated power

As a function of the ambient temperature

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

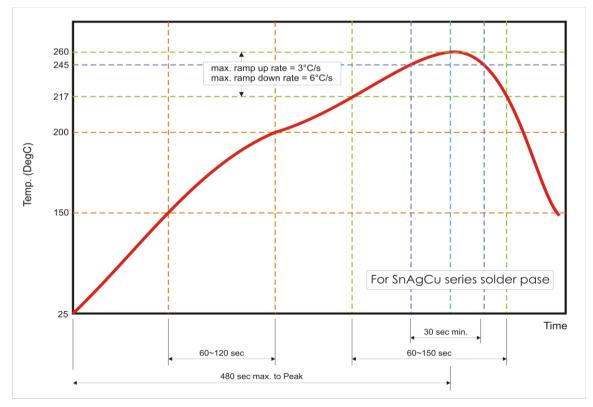


Fig 3. Infrared soldering profile for Chip Resistors



CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WF10	Α	1202	F	Т	L
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WF10: 1210	A: Power 3/4W	5% , E24: 2 significant digits followed by no. of zeros $100\Omega = 101$ _ $10K\Omega = 103$ 1% , E24+E96: 3 significant digits followed by no. of zeros $100\Omega = 1000$ $37.4K\Omega = 3742$	F: ± 1% J: ± 5%	T: 7" Reeled taping	L = Sn base (lead free)

Reeled tape packaging: 8mm width paper taping 5000pcs per 7" reel.

TEST AND REQUIREMENTS(JIS C 5201-1: 1998)

Basic specification:

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 68-1, subclause 5.3, unless otherwise specified.

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

TEST	PROCEDURE / TEST METHOD	REQUIREMENT			
1231	PROCEDURE/ 1231 WEITHOD	Resistor			
DC resistance	DC resistance values measured at the test voltages specified				
Clause 4.5	below:	Within the specified tolerance			
	$<10\Omega@0.1V$, $<100\Omega@0.3V$, $<1K\Omega@1.0V$,				
	<10KΩ@3V, <100KΩ@10V, <1MΩ@25V, <10MΩ@30V				
Temperature	Natural resistance change per change in degree centigrade.	Refer to			
Coefficient of Resistance(T.C.R)	$R_2 - R_1 \times 10^6 \text{ (ppm/sC)} + 20^{\circ}\text{C} + 5^{\circ}\text{C} + 10^{\circ}\text{C}$	"QUICK REFERENCE DATA"			
Clause 4.8	$\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} t_1 : 20\text{°C+5°C-1°C}$				
	R ₁ : Resistance at reference temperature				
	R ₂ : Resistance at test temperature				
Short time	Permanent resistance change after a 2 second application of	1D/D (440/ 0.050)			
overload (S.T.O.L)	a voltage 2.5 times RCWV or the maximum overload voltage	Δ R/R max. \pm (1%+0.05 Ω)			
Clause 4.13	specified in the above list, whichever is less.				
Resistance to soldering heat (R.S.H)	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at $260^{\circ}\text{C}\pm5^{\circ}\text{C}$	Δ R/R max. \pm (1%+0.05 Ω) no visible damage			
Clause 4.18					
Solderability	Un-mounted chips completely immersed for 2±0.5 second in	95% coverage min., good tinning and			
Clause 4.17	a SAC solder bath at 235℃ ±5℃	no visible damage			

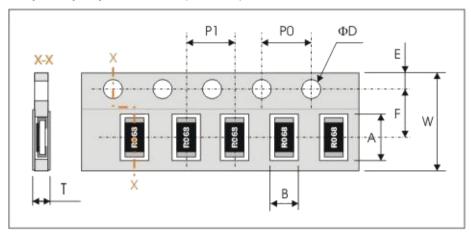


PROCEDURE / TEST METUOR	REQUIREMENT			
PROCEDURE / TEST METHOD	Resistor			
es at -55°C±3°C, 2~3 minutes at 20°C+5°C-1°C, 30 at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total ous cycles	Δ R/R max. ±(1%+0.05 Ω)			
1000 +48/-0 hours, loaded with RCWV or Vmax continuously in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, $\Delta R/R \text{ max.} \pm (3\% + 0.109)$				
/-0 hours; loaded with RCWV or V _{max} in chamber 70±2°C, 1.5 hours on and 0.5 hours off	Δ R/R max. ±(3%+0.10Ω)			
mounted on a 90mm glass epoxy resin PCB(FR4), once 3mm for 10sec.	No visual damaged, Δ R/R max. \pm (1%+0.05 Ω)			
ring force: 5N, Test time: 10±1sec.	No remarkable damage or removal of the terminations			
maximum overload voltage (DC) for 1minutes	R≧1GΩ			
Apply the maximum overload voltage (AC) for 1 minutes voltage Clause 4.7				
ma	ximum overload voltage (AC) for 1 minutes			



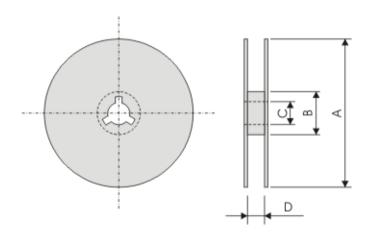
PACKAGING

Paper Tape specifications (unit :mm)



Component Size / Series	W		F	Е		P0		ΦD
WF10A	8.00±0.30	3	3.50±0.20	1.75±0.10		4.00±0.10		Φ 1.50 $^{+0.1}_{-0.0}$
Component Size / Series	А		В			P1		Т
WF10A	3.60±0.20		3.00±0.20		4.00±0.10			Max. 1.0

Reel dimensions



Symbol	Α	В	С	D	
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5	

Taping Quantity

- Chip resistors 5,000 pcs/reel