

APPROVAL SHEET

WR20, WR25

±1%, ±5%

General purpose chip resistors

Size 2010, 2512

(Automotive)

FEATURE

- 1. Automotive grade AEC Q-200 compliant
- 2. 100% CCD inspection
- 3. RoHS compliant and Lead free products

APPLICATION

- Automotive application
- · Consumer electrical equipment
- EDP, Computer application
- Telecom application

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

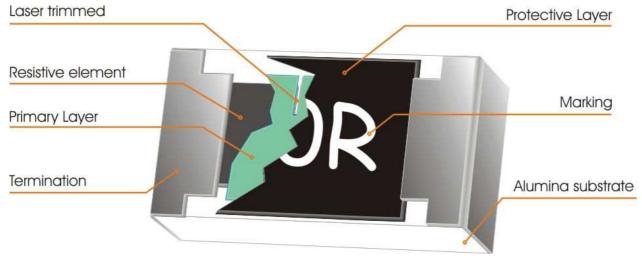


Fig 1. Construction of 2512, 2010 Chip-R

QUICK REFERENCE DATA

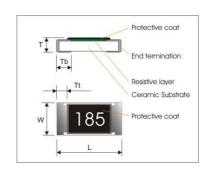
Item	General Sp	pecification	
Series No.	WR20	WR25	
Size code	2010 (5025),	2512(6432)	
Resistance Tolerance	±5% (E24); ±1	% (E24+E96)	
Resistance Range	1Ω ~ 10MΩ,	Jumper (0Ω)	
TCR (ppm/°C) $R \le 10\Omega$	± 200 ppm/°C	± 200 ppm/°C	
10Ω < R ≤ 1MΩ	± 100 ppm/°C	± 100 ppm/°C	
> 1MΩ	± 200 ppm/°C	± 200 ppm/°C	
Max. dissipation at T _{amb} =70°C	0.5 W	1W	
Max. Operation Voltage (DC or RMS)	200V	250V	
Max. Overload Voltage (DC or RMS)	400V	500V	
Climatic category (IEC 60068)	55/155/56		

Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by $RCWV = \sqrt{Rated Power \times Resistance Value} \text{ or Max. RCWV listed above, whichever is lower.}$
- 3. The resistance of Jumper is defined $<0.05\Omega$.

DIMENSIONS (unit: mm)

TYPE	WR20	WR25	
L	5.00±0.20	6.40±0.20	
W	2.50±0.20	3.20±0.20	
Т	0.55±0.10	0.60±0.10	
Tt	0.65±0.25	0.65±0.25	
Tb 0.60±0.25		0.90±0.25	



MARKING

Size	±5%	±1%	
2512, 2010	4-digits marking	4-digits marking	

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of $\pm 5\%$ & $\pm 1\%$. The values of the E24/E96 series are in accordance with "IEC publication 60063".

Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

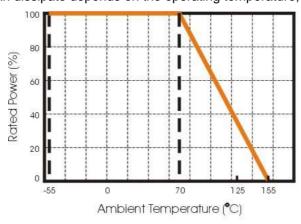


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems. Chip placement can be on ceramic substrates and printed-circuit boards (PCBs). Electrical connection to the circuit is by individual soldering condition. The end terminations guarantee a reliable contact.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

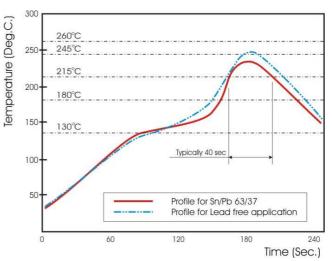


Fig 3. Infrared soldering profile for Chip Resistors

CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

WR25	x	472_	J	Т	L	J
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination	Special code
WR25 : 2512	X:	5%, E24: 2 significant digits	F:±1%	T: 7" Reeled taping	code	J = Automotive grade
WR20: 2010	±5%, 1R ~ 10MR	followed by no. of zeros	J : ±5%	Q : 10" Reeled taping	L = Sn base (lead free)	and Anti-Sulfuration 10ppm x 240hrs
	±1%, 10R ~ 1MR	$100\Omega = 101_{-}$	P: Jumper	G : 13" Reeled taping	(load free)	10ppiii x 2 101iii0
	W:	$10K\Omega = 103$		B : Bulk		
	±1%,< 10R; > 1MR	1%, E24+E96: 3 significant digits followed by no. of				
		zeros				
		100Ω =1000				
		37.4KΩ =3742				

TEST AND REQUIREMENTS

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied:

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

TEST	TEST PROCEDURE / TEST METHOD		REQUIREMENT		
TEST	PROCEDURE/TEST METHOD	Resistor	0Ω		
Electrical Characteristics	- DC resistance values measurement	Within the specified tolerance			
	- Temperature Coefficient of Resistance (T.C.R)	Refer to "QUICK REFERENCE			
JISC5201-1: 1998	Natural resistance change per change in degree centigrade.	DATA"			
Clause 4.8	$\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)} t_1 : 20 \text{°C} + 5 \text{°C} - 1 \text{°C}$		<50mΩ		
	R ₁ : Resistance at reference temperature				
	R ₂ : Resistance at test temperature				
Resistance to soldering	Un-mounted chips completely immersed for 10±1second in a	Δ R/R max. \pm (1.0%+0.05 Ω)			
heat(R.S.H)	SAC solder bath at 270°C ±5°C	no visible damage	<50mΩ		
MIL-STD-202 method 210					
Solderability	a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C/ 5sec.	95% coverage min., good tinnir	ng and no		
J-STD-002	b) Steam the sample dwell time 1 hour/ solder dipping	visible damage			
	260°C / 7sec.				
Temperature cycling	1000 cycles, -55°C \sim +155°C, dwell time 5 \sim 10min	Δ R/R max. \pm (1.0%+0.05 Ω)			
JESD22		No visible damage	<50mΩ		
Method JA-104					

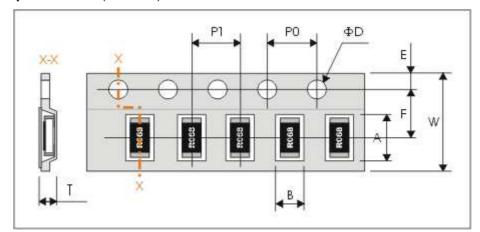
TEST	DDOCEDURE / TEST METUOD	REQUIREMENT	REQUIREMENT		
TEST	PROCEDURE / TEST METHOD	Resistor	0Ω		
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ		
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	Δ R/R max. \pm (2.0%+0.10 Ω) No visible damage	<50mΩ		
Operational Life 1000+48/-0 hours; 35% of operation power, 125±2°C MIL-STD-202 method 108		Δ R/R max. \pm (2.0%+0.1 Ω) No visible damage	<50mΩ		
High Temperature Exposure MIL-STD-202 Method 108	1000+48/-0 hours; without load in a temperature chamber controlled 155±3°C	Δ R/R max. \pm (2.0%+0.10 Ω) No visible damage	<50mΩ		
Board Flex AEC-Q200-005	Resistors mounted on a 90mm glass epoxy resin PCB(FR4),bending once 2mm for 10sec.	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ		
Terminal strength AEC-Q200-006	Pressurizing force: 1Kg, Test time: 60±1sec.	No remarkable damage or removal of the terminations			
Thermal shock MIL-STD-202 method 107	Test -55 to 155°C/ dwell time 15min/ Max transfer time 20sec 300cycles	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ		
ESD AEC-Q200-002	Test contact 1.0KV	Δ R/R max. \pm (1.0%+0.05 Ω) No visible damage	<50mΩ		
Short Time Overload JISC5201-1: 1998 Clause 4.13	2.5 times RCWV or max. overload voltage, for 5seconds	Δ R/R max. \pm (2.0%+0.10 Ω) No visible damage	<50mΩ		
Load life in Humidity JISC5201-1: 1998 Clause 4.24	sc5201-1: 1998 chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off		<50mΩ		
Load life (endurance) JISC5201-1: 1998 Clause 4.25 1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off		Δ R/R max. \pm (2%+0.10 Ω) No visible damage	<50mΩ		

TEST CONDITION FOR JUMPER (0 Ω)

	` '		
Type	WR20X	WR25X	
Power Rating At 70C	1/2 W	1 W	
Resistance	Max.	50mR	
Rated Current	3.2 A	4.5 A	
Peak Current	8 A	11 A	
Operating Temperature	ng Temperature -55C ~ 155C		

PACKAGING

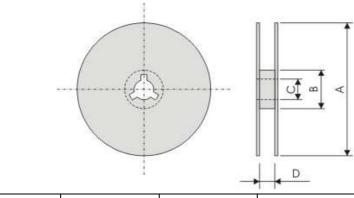
Plastic Tape specifications (unit :mm)



Type	Α	В	W	F	E
WR20	5.50±0.20	2.80±0.20	12.00±0.30	5.50±0.10	1.75±0.10
WR25	6.90±0.20	3.60±0.20	12.00±0.30	3.30±0.10	1.73±0.10

Туре	P1	P0	ΦD	Т
WR20	4.00±0.10	4.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	MAX1.2
WR25	4.00±0.10	4.00±0.10	$\Psi 1.50_{-0.0}$	IVIAA 1.2

Reel dimensions



Symbol	Α	В	С	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	14.0±0.2

Taping quantity

WR20, WR25 by plastic tape taping 4,000 pcs per reel.