

APPROVAL SHEET

TA06

±1%, ±0.5%, ±0.25%, ±0.1%, TC25, TC50

High Precision Thin Film Chip Resistors

Size 064R







*Contents in this sheet are subject to change without prior notice.



FEATURE

- 1. SMD metal film resistor
- 2. High reliability and stability of 0.1% and below per customer request
- 3. High performance of TCR: 25 ppm/K and below per customer request
- 4. Low current noise
- 5. +/-0.02% is upon the customer request.
- 6. TCR tracking down to 5 ppm/°C
- 7. Tolerance tracking down to 0.05%

APPLICATION

- Medical equipment
- · Measuring instrument
- Communication device
- · Precision analogue circuits
- Voltage divider

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive layer that is applied to the top surface of the substrate. The composition of the resistive layer is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For environmental soldering issue, the outer layer of these end terminations is a Lead-free solder.

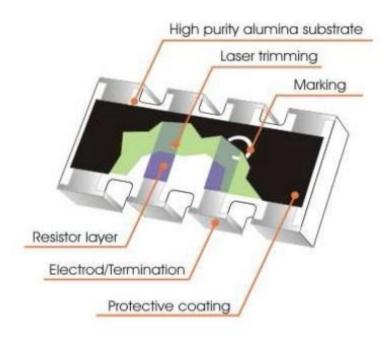


Fig 1. Construction of Chip-R TA06



QUICK REFERENCE DATA

Ite	em	General Specification
Serie	es No.	TA06
Si	ize	0603×4
Termination	construction	Convex
Resistance	e tolerance	±1%, ±0.5%, ±0.25%, ±0.1%
Resistance Range	Equal values/ Two pairs /Different values *	20R~200K
TCR (p	opm/°C)	±25 ppm/°C, ±50 ppm/°C
Max. dissipation	on at T _{amb} =70°C	0.1 watts/element @70°C 0.4 watts/package @70°C
Max. opera	tion voltage	75 V
Max. overl	oad voltage	150 V
Operating ⁻	Temperature	- 55~ +155C

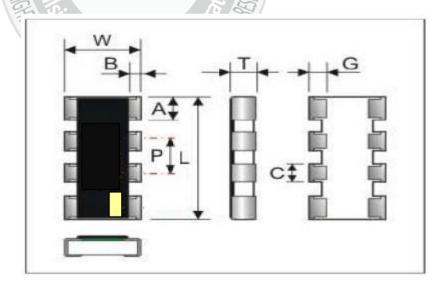
^{*}Detailed resistance configuration can be request from sales.

Note:

- This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- Max. Operation Voltage: So called RCWV (Rated Continuous Working Voltage) is determined by
 RCWV = √RatedPower×Resistance Value or Max. RCWV listed above, whichever is lower.

DIMENSIONS:(unit:mm)

Туре	TA06
W	1.50 ± 0.15
L	3.20 ± 0.15
Р	0.80 ± 0.10
С	0.40 ± 0.10
Α	0.60 ± 0.10
В	0.40 ± 0.15
G	0.30 ± 0.15
Т	0.40 ± 0.04





MARKING

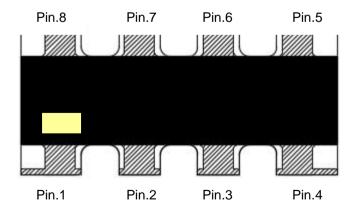
• Array Marking for Equal values

3-digits marking for array product TA06 has same marking rule as WF06

3-digits marking

Nominal resistance					Description										
1.E-24 series				As <i>0603 WR06X</i> ±5%.											
2.E-96 s	series			value :	X=10 ⁻¹ ,			C=10 ² , 25X,17		E=10⁴ , F , 1K78	==10 ⁵ =25B	d code is	the inde	x of resis	stance
3. Rema	ark			There is	no markii	ng for th	e items a	re not ur	nder E-24	and E-9	6 series				
CODE	R_value	CODE	R_value	CODE	R_Value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	547	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

- No Marking for Two pairs / Different Values
- Marking in Pin.1 for direction check.





FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E192 & E24 series for resistors with a tolerance of $\pm 1, \pm 0.5\%, \pm 0.25\%, \pm 0.1\%$. The values of the E24/E192 series are in accordance with "IEC publication 60063".

DERATING

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

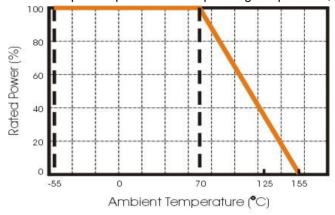


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

MOUNTING

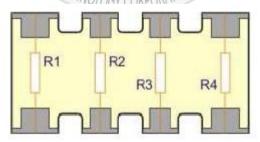
Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

CONSTRUCTION



Construction	Array	Two pair*	Different values*
Resistance	R1 = R2 = R3 = R4	R1 = R2 ≠ R3=R4	R1≠ R2 ≠ R3 ≠R4

^{*}Detailed resistance configuration can be request from sales.



SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds within lead-free solder bath. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering profile and condition that provide reliable joints without any damage are given in Fig 3. and Table 1.

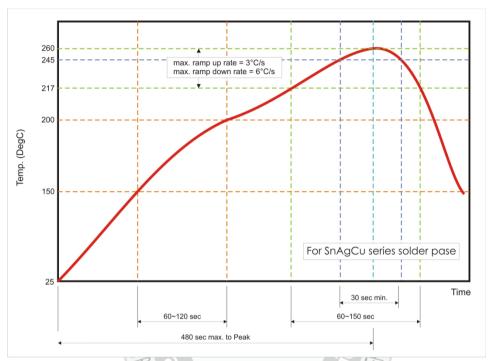


Fig. 3 Infrared soldering profile for Chip Resistors

Table 1. Infrared soldering condition for Chip Resistors

Temperature Condition	Exposure Time
Average ramp-up rate (217°C to 260°C)	Less than 3°C/second
Between 150 and 200°C	Between 60-120 seconds
> 217°C	Between 60-150 seconds
Peak Temperature	260°C +0/-5°C
Time within 245°C	Min. 30 seconds
Ramp-down rate (Peak to 217°C)	Less than 6°C/second
Time from 25°C to Peak	No greater than 480 seconds



CATALOGUE NUMBERS

The resistors have a catalogue number starting with

TA	06	M	xxxx	D	Т	L	Т
Substrate code TA : convex	size code 06 : 0603	TC and number of resistors M = 4R, TC50 N = 4R, TC25	Resistance code (Array) 3 significant digits followed by no. of zeros $102\Omega = 1020$ $37.4K\Omega = 3742$ $220\Omega = 2200$	Tolerance F:±1% D:±0.5% C:±0.25% B:±0.1%	Packaging code T: Reeled	Termination code L: Sn base (lead free)	Customer Code D: Equal values B: Two pairs. T: Different Values

- 1. Reeled tape packaging: 8mm width paper taping.
- 2. 5,000 pcs/reel for TA06





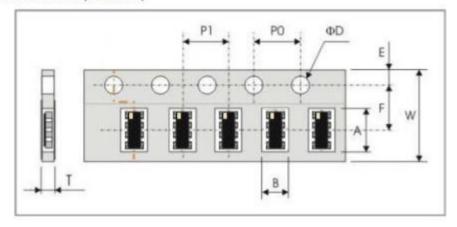
TEST AND REQUIREMENTS(JIS C 5201-1: 1998)

TEST	DDOCEDURE	REQUIREMENT
TEST	PROCEDURE	Resistor
DC resistance Clause 4.5	DC resistance values measured at the test voltages specified below : $<10\Omega@0.1V, <100\Omega@0.3V, <1K\Omega@1.0V, \\ <10K\Omega@3V, <100K\Omega@10V, <1M\Omega@25V, <10M\Omega@30V$	Within the specified tolerance
Temperature Coefficient of Resistance(T.C.R) Clause 4.8	Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1(t_2-t_1)}\times 10^6 \ \text{(ppm/°C)}$ $\text{R}_1: \text{Resistance at reference temperature}$ $\text{R}_2: \text{Resistance at test temperature}$ $\text{t}_1: 20^\circ\text{C}+5^\circ\text{C}-1^\circ\text{C}$ $\text{t}_2: 125^\circ\text{C}+5^\circ\text{C}-1^\circ\text{C}$	Refer to "QUICK REFERENCE DATA"
Short time overload (S.T.O.L) Clause 4.13	Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	Δ R/R max. \pm (0.2%+0.05 Ω)
Resistance to soldering heat(R.S.H) Clause 4.18	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at $260^{\circ}\!\text{C}\pm\!5^{\circ}\!\text{C}$	no visible damage Δ R/R max. $\pm (0.1\% + 0.05\Omega)$
Solderability Clause 4.17	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at 235 $^{\circ}\!\text{C}$ ±5 $^{\circ}\!\text{C}$	good tinning (>95% covered) no visible damage
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C-1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	no visible damage $\Delta R/R$ max. $\pm (0.25\% + 0.05\Omega)$
Endurance Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	Δ R/R max. \pm (0.5%+0.05 Ω)
Load life in Humidity Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	Δ R/R max. \pm (0.5%+0.05 Ω)
Bending strength Clause 4.33	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 3 mm, once for 10 seconds.	Δ R/R max. \pm (0.1%+0.05 Ω)
Adhesion Clause 4.32	Pressurizing force: 5N, Test time: 10±1sec.	No remarkable damage or removal of the terminations.
Insulation Resistance	Apply the maximum overload voltage (DC) for 1minute	R≥10GΩ
Clause 4.6		
Dielectric Withstand	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover
Voltage		
Clause 4.7		



PACKAGING

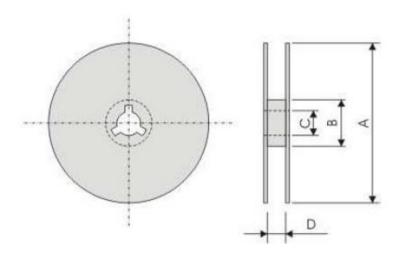
Paper Tape specifications (unit :mm)



Symbol	А	В	W	F	E
TA06	3.60±0.20	2.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10

Symbol	P1	P0	ΦD	Т
TA06	4.00±0.10	4.00±0.10	$\Phi 1.50^{+0.1}_{-0.0}$	Max. 1.0

Reel dimensions



Symbol	Α	В	С	D
TA06	F178.0±2.0	F60.0±1.0	13.0±0.2	9.0±0.5