

# APPROVAL SHEET

# WLPN202012 Series Shielded SMD Power Inductors

\*Contents in this sheet are subject to change without prior notice.



#### **Features**

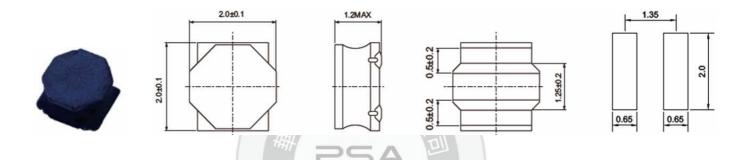
- 1. Close magnetic loop with magnetic resin shielded.
- 2. Low profile, High inductance.

### **Applications**

- 1. General propose power inductor in DC power system.
- 2. Inductor in DC/DC converter.
- 3. Low profile for portable and wearable device.
- 4. LC filter in Audio D class Amplifier.

#### **Shape and Dimension**

Unit: mm



## **Ordering Information**

WL	PN	2020	12/100	OW (N)(D)	1R0	Р	В
Product Code	Series	Dimensions	Thickness	Tolerance	Value	Packing Code	
WL: Inductor	Shielded SMD Power Inductors	2.0 * 2.0 mm	1.2 mm	M: ± 20% N: ± 30%	1R0 = 1.0uH	P=7" Reeled (Embossed tape)	B:STD



#### **Electrical Characteristics**

WLPN202012	L	Inductance	D.C.R ±20%(Ω)	Test Freq (KHz)	Rated Current(mA)			
Series	(uH)	Tolerance			Saturation Current Idc1 (Typ.)	Temperature Rise Current Idc2 (Typ.)	Saturation Current Idc1(Max.)	Temperature Rise Current Idc2(Max.)
WLPN202012N1R0PB	1.0	±30%	0.070	100	2050	1850	1900	1700
WLPN202012N1R5PB	1.5	±30%	0.090	100	1800	1650	1650	1500
WLPN202012M2R2PB	2.2	±20%	0.107	100	1500	1500	1350	1370
WLPN202012M3R3PB	3.3	±20%	0.190	100	1150	1100	1000	1020
WLPN202012M4R7PB	4.7	±20%	0.241	100	1050	1000	900	910

- 1. Test Frequency: 100 KHz.
- 2. Test Equipment:

Inductance: Chroma3302+1320+16502 or equivalent.

DCR: Chroma16502 or equivalent.

- 3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.
- 4. Temperature rise current ldc2: The value of current causes a 40°C temperature rise.
- 5. Rated Current: Either Idc1 or Idc2 whichever is smaller.
- 6. Operating Temperature Range:-25°C to +125°C (Including self-temperature rise).
- 7. Storage Temp. Range :  $-40^{\circ}$ C to  $+85^{\circ}$ C.
- 8. MSL: Level 1.

## **Structural Drawing**



① Ferrite core : Ni-Zn ferrite.

② Winding wire: Polyurethane-copper wire.

③ Over-coating resin: Epoxy resin, containing ferrite powder.

④ Electrode : External electrode (substrate)
Ag

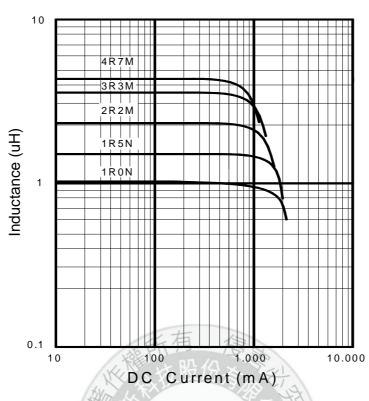
External electrode (base plating) Ni-Sn

External electrode (top surface solder coating) Sn-Ag-Cu



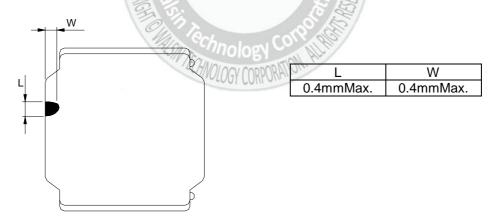
#### **Characteristic Curve**

Inductance vs. DC Current



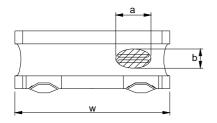
## **Core Chipping:**

The appearance standard of the chipping size in top side, of bottom side ferrite core is following dimension



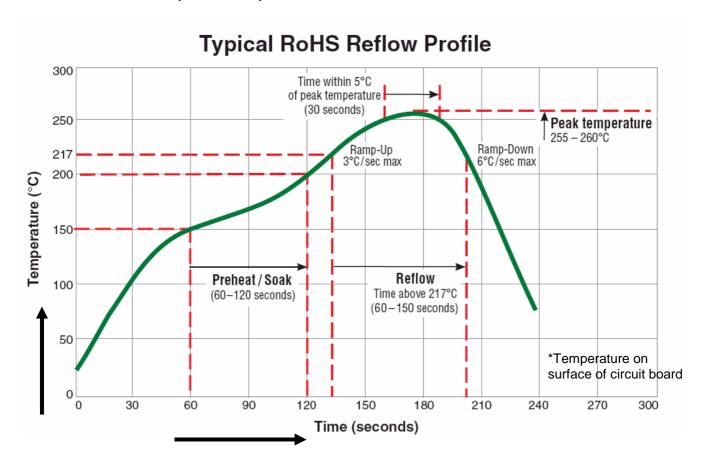


Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2
  Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
- ③ When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

#### **Reflow Profile Chart (Reference):**



(Table 1)

The products may be exposed to reflow soldering process of above profile up to two times.



### Mechanical Performance /Environmental Test Performance Specifications: (WLPN202012 series)

No.	Item	Test condition	Requirements						
	Resistance to Deflection.	No damage.	soldering conditions As illustrated below	nall be soldered to the test board by the reflow s show in Table 1. , apply force in the direction of the Arrow indicating e test board Reaches to 2 mm.					
1			1 Force Rod	December 20					
			45±2	Sample					
			Test board size :100x40x10 Test board material I: glass epoxy-resin Solder cream thickness:0.1  Land dimensions Unit: mm						
	Adhesion of Terminal Electrode.	Shall not come off PC board.	The test samples sh soldering conditions	nall be soldered to the test board by the reflow shown in Table 1.					
2		two to	101						
		W	Solder cream thickr (Refer to recommer "Precaution".)	nded Land Pattern Dimensions Defined in					
3	Body strength.	No damage.	Applied force :20 N. Duration :10 s.	Sample					
		ii .	0.6\	•					
	Resistance to Vibration.	△L/L:within±10%  No abnormality  observed In  appearance.		nall be soldered to the test board by the reflow ns shown in Table 1.Then it shall be submitted litions.					
4			Frequency range	10Hz~55Hz					
			Total Amplitude	1.5mm(May not exceed acceleration 196 m/S2)					
			Sweeping Method	10Hz to 55Hz to 10 Hz for 1 min.					
			Time	For 2 hours on each X, Y, and Z axis.					
5	Resistance to Soldering heat (Reflow).	△L/L:within±10% No abnormality observed In appearance.	40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times.						
			Test board thickness: 1.0 mm. Test board material: glass epoxy-resin.						

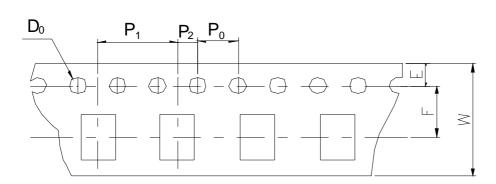


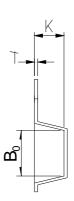
	Solder ability.	At least 90% of surface of terminal		samples shall			then Immerse	ed in	
		electrode is	molten solder as shown in below table. Flux: Methanol solution containing rosin 25%.				<u></u>		
6		covered by new	Solder Temperature 245±deg C						
		solder.		Time	5±1.0 S.				
			Immersing Speed		25 mm/s				
	Tomporoturo	A. I. /I			ance shall be taken at temperati			data ta	
7	Temperature Characteristics.	△L/L:within±20% No abnormality observed in appearance.	-25 deg (	C to +85 deg C rence to induc	<b>).</b>		-	_	
	Thermal shock.	△L/L:within±10% No abnormality observed in appearance.	soldering The test s sequence The temp	erature cycle	own in Tabl be placed a shall be rep	e 1. It specified s	shown in belo		
8			Condition	s of steps for	1 cycle.				
			Step Temperature			Time(r			
			1	-40±3 deg C		30±	3		
			2	Room Te		3 maxir			
			3	85±2 de		30±			
			4	Room Te		3 maxir			
9	Low Temperature life Test.	△L/L:within±10% No abnormality observed in appearance.	soldering After that in below Tem	samples shall conditions show that test samples table.  perature  Time	own in Tabl	e 1. e placed at t deg C	-		
10	Loading at high temperature life test.	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specifi temperature and applied the rated current continuously as shown in below table.  Temperature  85±2 deg C						
		120/1	Ten	perature	1		-		
			Appli	ed current		current Page 3)			
		A .		Time	•	24/-0 h	-		
11	Damp heat life test.	△L/L:within±10% No abnormality observed in appearance.	The test s soldering The test s temperate	samples shall conditions shall amples shall ure and humid	be soldered own in Tabl be placed in lity as show	I to the test I e 1. n thermostat	tic oven set a		
				umidity		5%RH	1		
				Time		24/-0 h	1		
	Loading under Damp heat life test.	Damp heat life No abnormality		samples shall conditions she samples shall ure and humid in below table	be soldered own in Tabl be placed in lity and app	I to the test I e 1. n thermostat lied the rate	tic oven set a	t specified	
12			Tem	perature		60±2 deg C			
			H	umidity		90~95%RH	RH		
			Applied current		Rated current (Refer to Page 3)				
				Time		500+24/-0 h	1		
			-			•			

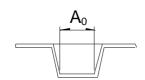


### **Tape & Reel Packaging Dimensions:**

Dimensions Unit: mm



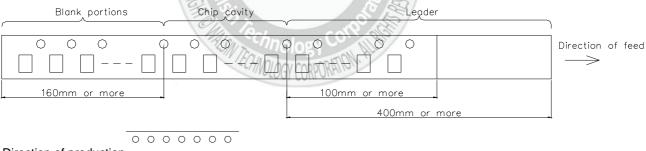


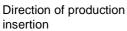


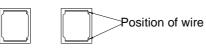
			_	-150	1			_		
$A_0$	$B_0$	W	F	以下	P <sub>1</sub> /	$P_2$	$P_0$	$D_0$	Т	K
2.2 ±0.09	2.2 ±0.09	8.0 ±0.2	3.5 ±0.1	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	4.0± 0.1	Φ1.5 +0.1 -0	0.25 ±0.05	1.3 ±0.05

### **Direction of rolling**

PASSIVE SYSTEM ALLIANCE

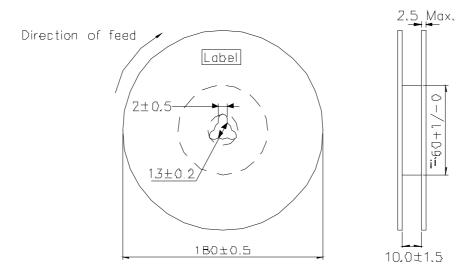








#### Reel



Label position:on the opposite sie of sprocket holes side of reel



Peel-off strength: 0.1N~0.7N

Peel-off angle:165°~180°

Peel-off speed: 300mm/mm

Quantity per reel: 2.5K pcs