

APPROVAL SHEET

WLPN303010 Series SMD Molded Power Choke Inductors

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*Contents in this sheet are subject to change without prior notice.

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Features

- 1. Close magnetic loop with magnetic resin shielded.
- 2. Low profile, High inductance.

Applications

- 1. General propose power inductor in DC power system.
- 2. Inductor in DC/DC converter.
- 3. Low profile for portable and wearable device.
- 4. LC filter in Audio D class Amplifier

Shape and Dimension

Unit: mm



Ordering Information

WL	PN	3030	4010LOG	CORPORINION	1R2	Р	В
Product Code	Series	Dimensions	Thickness	Tolerance	Value	Packing Code	
WL: Inductor	Shielded SMD Power Inductors	3.0 * 3.0 mm	1.0 mm	M: ± 20% N: ± 30%	1R2 = 1.2uH 100 = 10uH	P=7" Reeled (Embossed Tape)	B:STD



Electrical Characteristics

	L (uH)	Inductance Tolerance				Rated Current	
WLPN303010			Test Freq (KHz)	DCR	SRF	(mA) Max	
Series				(Ω ± 20%)	(MHz)Min	Saturation Current Idc1	Temperature Rise Current Idc2
WLPN303010N1R2PB	1.2	N	100	0.065	120	1700	1480
WLPN303010N1R5PB	1.5	Ν	100	0.075	99	1440	1370
WLPN303010M2R2PB	2.2	М	100	0.083	86	1300	1300
WLPN303010M3R3PB	3.3	М	100	0.130	64	1000	1030
WLPN303010M4R7PB	4.7	М	100	0.170	50	850	900
WLPN303010M6R8PB	6.8	М	100	0.250	44	700	745
WLPN303010M100PB	10	М	100	0.350	34	600	620
WLPN303010M150PB	15	М	100	0.550	25	450	480
WLPN303010M220PB	22	М	100	0.770	22	380	410

1. Test Frequency: 100KHz.

2. Test Equipment:

Inductance: Chroma3302+1320+16502 or equivalent. DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current ldc2: The value of current causes a 40 $^\circ\!\mathrm{C}$ temperature rise.

5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

6. Operating Temperature Range:-25 $^\circ\!\mathrm{C}$ to +120 $^\circ\!\mathrm{C}$ (Including self-temperature rise).

7. Storage Temp. Range : -40°C to +85°C.

8. MSL : Level 1.

Structural Drawing



- ① Ferrite core : Ni-Zn ferrite.
- ② Winding wire : Polyurethane-copper wire.
- ③ Over-coating resin : Epoxy resin, containing ferrite powder.
- ④ Electrode : External electrode (substrate)
 Ag
 External electrode (base plating)
 Ni-Sn

External electrode (top surface solder coating) Sn-Ag-Cu



Characteristic Curve



Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite core is following dimension.





Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
 ③ When total area of exposed wire occurring to each sides is
 - not greater than 50% of coating resin area, that is acceptable.

Reflow Profile Chart (Reference):



(Table 1)

The products may be exposed to reflow soldering process of above profile up to two times.

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Mechanical Performance /Environmental Test Performance Specifications: (WLPN303010 series)

No.	Item	Test condition	Requirements				
	Resistance to Deflection.	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.				
1			$R5 \xrightarrow{20}_{Force Rod} Board \\ R5 \xrightarrow{45\pm2}_{45\pm2} 45\pm2 $ $Land dimensions \\ Unit: mm$				
			Solder cream thickness:0.1				
	Adhesion of Terminal Electrode.	Shall not come off PC board.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.				
2			Applied force: 10 N to X and Y directions Duration: 5 s. Solder cream thickness:0.1 mm. (Refer to recommended Land Pattern Dimensions Defined in "Precaution")				
3	Body strength.	No damage.	Applied force :20 N. Duration :10 s.				
	Resistance to Vibration.	△L/L:within±10% No abnormality observed In	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.Then It shall be submitted to below test conditions.				
		appearance.	Frequency range 10Hz~55Hz				
4			Sweeping Method 10Hz to 55Hz to 10 Hz for 1 min				
			TimeFor 2 hours on each X, Y, and Z axis.				
5	Resistance to Soldering heat (Reflow).	△L/L:within±10% No abnormality observed In appearance	The test sample shall be exposed to reflow oven at 230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times. Test board thickness: 1.0 mm.				
	Test board material: Glass epoxy-resin.						

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6	Solder ability.	At least 90% of surface of terminal electrode is covered by new solder.	The test samples shall molten solder as showr Flux: Methanol solution Solder Temperature Time Immersing Speed		be dipped in flux, and the in below table. containing rosin 25%. 245±deg C 5±1.0 S. 25 mm/s		hen Immers	ed in	
7	Temperature Characteristics.	△L/L:within±20% No abnormality observed in appearance.	Measurement of inductance shall be taken at temperature range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, change rate shall be calculated.						
	Thermal shock.	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified shown in below table in sequence. The temperature cycle shall be repeated 100 cycles.						
8			Conditio	ns of steps for 7					
			Step Temperature		ture	Time(r	nin) 2		
			1		y C		3		
			2	85+2 dec	n C	30+	2		
			4	Room Te	mp	3 maxir	mum		
	Low	∧L/L:within±10%	The test samples shall be soldered to the test board by the reflow						
9	Temperature life Test.	No abnormality observed in appearance.	soldering conditions shown in Table 1. After that, the test samples shall be placed at test conditions as shown in below table. Temperature -40±2 deg C Time 500 +24/-0 h						
10	Loading at high temperature life test.	△L/L:within±10% No abnormality observed in appearance.	The test soldering The test temperat below ta Ter Appl	samples shall t g conditions sho samples shall t ture and applied ble. nperature ied current Time	be soldere bwn in Tab be placed d the rated 85±2 Rated (Refer t 500+	d to the test h ble 1. in thermostat d current cont 2 deg C d current to Page 3) -24/-0 h	board by the	at specified shown in	
	Damp heat life	\triangle L/L:within±10%	The test samples shall be soldered to the test board by the reflow						
11	test.	No abnormality observed in appearance.	soldering The test temperat Ter	g conditions sho samples shall t ture and humidi nperature lumidity	own in Table 1. be placed in thermostatic lity as shown in below tab 60±2 deg C 90~95%RH		ic oven set able.	at specified	
				Time	500+	-24/-0 h			
12	Loading under Damp heat life test.	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.The test samples shall be placed in thermostatic oven set at specific temperature and humidity and applied the rated current continuous as shown in below table.Temperature60±2 deg CHumidity90~95%RHApplied currentRated current (Refer to Page 3)						
				Time		500+24/-0 h)		

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Tape & Reel Packaging Dimensions:

Dimensions Unit : mm













Reel



Label position:on the opposite sie of sprocket holes side of reel



Quantity per reel : 2K pcs

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