

# APPROVAL SHEET

# WLPN404010 Series Shielded SMD Power Inductors

\*Contents in this sheet are subject to change without prior notice.



#### **Features**

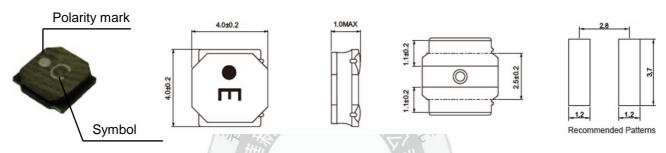
- 1. Close magnetic loop with magnetic resin shielded.
- 2. Low profile, High inductance.

#### **Applications**

- 1. General propose power inductor in DC power system.
- 2. Inductor in DC/DC converter.
- 3. Low profile for portable and wearable device.
- 4. LC filter in Audio D class Amplifier.

#### **Shape and Dimension**

Unit: mm



# **Ordering Information**

WL	PN	4040	10	No	1R0	L	В
Product Code	Series	Dimensions	Thickness	Tolerance	Value	Packing Code	
WL: Inductor	Shielded SMD Power Inductors	4.0 * 4.0 mm	1.0 mm	M: ± 20% N: ± 30%	1R0 = 1.0uH 100 = 10uH	L=13" Reeled (Embossed Tape)	B:STD



#### **Electrical Characteristics**

WLPN404010	L	Ob. al	Inductance	Test	DCR (Ω±20%)	SRF	110000	d Current A) Max	
Series	(uH)	Symbol	Tolerance	Freq (KHz)		(MHz)Min	Saturation Current Idc1	Temperature Rise Current Idc2	
WLPN404010N1R0LB	1.0	А	±30%	100	0.056	116	2000	1900	
WLPN404010M2R2LB	2.2	С	±20%	100	0.085	73	1200	1500	
WLPN404010M3R3LB	3.3	Е	±20%	100	0.100	58	1100	1400	
WLPN404010M4R7LB	4.7	Н	±20%	100	0.140	47	950	1200	
WLPN404010M6R8LB	6.8	I	±20%	100	0.200	38	800	1000	
WLPN404010M100LB	10	K	±20%	100	0.300	31	620	750	
WLPN404010M150LB	15	М	±20%	100	0.430	24	540	600	
WLPN404010M220LB	22	N	±20%	100	0.570	19	450	500	

1. Test Frequency: 100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502 or equivalent.

DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current ldc2: The value of current causes a 40℃ temperature rise.

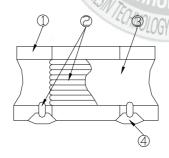
5. Rated Current: Either ldc1 or ldc2 whichever is smaller.

6. Operating Temperature Range:-25°C to +125°C (Including self-temperature rise)

7. Storage Temp. Range :  $-40^{\circ}$ C to  $+85^{\circ}$ C.

8. MSL: Level 1

#### **Structural Drawing**



① Ferrite core : Ni-Zn ferrite

② Winding wire: Polyurethane-copper wire

3 Over-coating resin: Epoxy resin, containing ferrite powder

④ Electrode : External electrode (substrate)
Ag

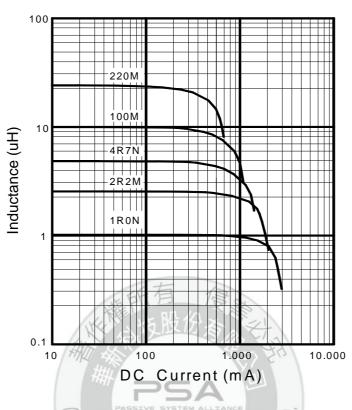
External electrode (base plating) Ni-Sn

External electrode (top surface solder coating) Sn-Ag-Cu



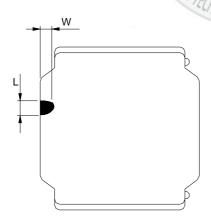
#### **Characteristic Curve**





# **Core Chipping:**

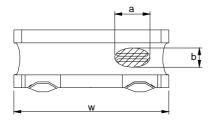
The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension.



L	W
1.0mmMax.	1.0mmMax.

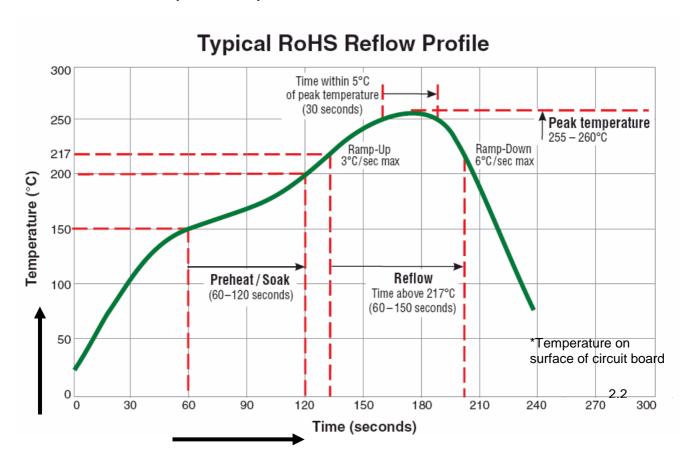


Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2
  Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
- ③ When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

### **Reflow Profile Chart (Reference):**



(Table 1)

The products may be exposed to reflow soldering process of above profile up to two times.



## Mechanical Performance /Environmental Test Performance Specifications: (WLPN404010 series)

No.	Item	Test condition	Requirements				
1	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1.  As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.  Porce Rod  Test Sample  Land dimensions Unit: mm				
2	Adhesion of Terminal Electrode	Shall not come off PC board	Test board material I: glass epoxy-resin Solder cream thickness:0.1  The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.  Applied force: 10 N to X and Y directions Duration: 5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")				
3	Body strength	No damage	Applied force :20 N Duration :10 s  R0.5mm Sample				
4	Resistance to Vibration	△L/L:within±10%  No abnormality observed In appearance	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then It shall be submitted to below test conditions  Frequency range 10Hz~55Hz  Total Amplitude 1.5mm(May not exceed acceleration 196 m/S2)  Sweeping Method 10Hz to 55Hz to 10 Hz for 1 min.  Time For 2 hours on each X, Y, and Z axis.				
5	Resistance to Soldering heat (Reflow)	△L/L:within±10% No abnormality observed In appearance	The test sample shall be exposed to reflow oven at 230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times.  Test board thickness:1.0 mm Test board material :glass epoxy-resin				



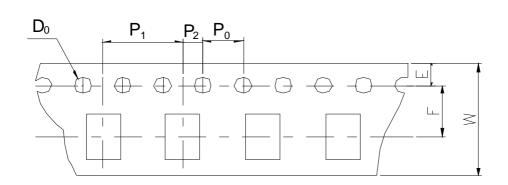
	Solder ability	At least 90% of		samples shall			hen Immers	ed in	
		surface of terminal electrode is	molten solder as shown in below table. Flux: Methanol solution containing rosin 25%						
6		covered by new		Temperature		5±deg C	$\neg$		
6		solder.	Time			±1.0 S.			
			Immersing Speed		25 mm/s		_		
			mineraling opeed		2.	3 11111/3			
7	Temperature Characteristics	△L/L:within±20% No abnormality observed In appearance	Range w With refe	Measurement of inductance shall be taken at temperature Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, change Rate shall be calculated.					
	Thermal shock	△L/L:within±10% No abnormality observed In appearance	By the re The test Shown in The temp	samples shall flow soldering samples shall below table in perature cycle	conditions be placed sequence shall be re	shown in Tal at specified e.	ble 1.		
8			Condition	ns of steps for	1 cycle				
			Step	Tempera		Time(r	,		
			1	-40±3 de	eg C	30±	3		
			2	Room Te		3 maxir	mum		
			3	85±2 de		30±	3		
			4	Room Te		3 maxir			
9	Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The reflo After that Condition Tem	samples shall w soldering co t, the test samp ns as shown in nperature Time	onditions sholes shall be below table -40±2	nown in Table be placed at t	1.		
10	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specif temperature and applied the rated current continuously as shown in below table.						
		~~\^?\S/\	Ten	nperature		deg C			
			Appl	ied current		l current o Page 3)			
		0		Time	-	·24/-0 h			
11	Damp heat life test   \( \triangle \L/L:\) within±10%   The test samples shall soldering conditions shall observed in appearance.   The test samples shall temperature and huming the soldering conditions of the test samples shall temperature and huming the soldering conditions of th		samples shall conditions sh samples shall	be soldere own in Tab be placed lity as shov	d to the test labeled to t	ic oven set a			
				lumidity		95%RH			
				Time		·24/-0 h			
	Loading under Damp heat life test	△L/L:within±10%  No abnormality observed in appearance.  The test samples shall be so soldering conditions shown in The test samples shall be platement temperature and humidity an as shown in below table.			be soldere own in Tab be placed lity and app	d to the test labeled to the test labeled the test labeled the rated	ic oven set a d current cor	t specified	
12				nperature		60±2 deg C	С		
			Humidity			90~95%RH			
			Applied current		Rated cu	Rated current (Refer to Page 3)			
			Time			500+24/-0 h			
				<del></del>					

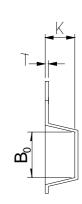


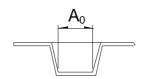
# **Tape & Reel Packaging Dimensions:**

Dimensions

Unit: mm



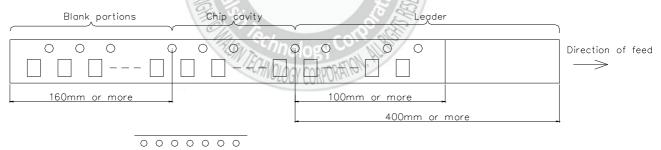




A <sub>0</sub>	B <sub>0</sub>	W	F	EGG	有P <sub>1</sub> ,	P <sub>2</sub>	P <sub>0</sub>	D <sub>0</sub>	Т	K
4.3 ±0.1	4.3 ±0.1	12.0 ±0.3	5.5 ±0.1	1.75 ±0.1	8.0 ±0.1	2.0 ±0.1	4.0 ±0.1	Φ1.5 +0.1 -0	0.3 ±0.05	1.4 ±0.1

## **Direction of rolling**

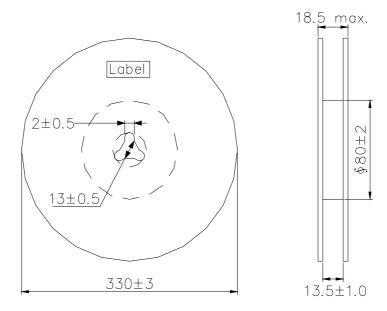




Direction of production insertion



#### Reel





Peel-off strength: 0.1N~1.3N Peel-off angle:165°~180° Peel-off speed: 300mm/mm

Quantity per reel: 5K pcs